



<b>FORM PTO-1449</b>  <b>U.S. DEPARTMENT OF COMMERCE</b> <b>PATENT AND TRADEMARK OFFICE</b>  <b>INFORMATION DISCLOSURE</b> <b>STATEMENT BY APPLICANT</b>  (Use several sheets if necessary)				<b>ATTY. DOCKET NO.</b> I-2-0183.1US		<b>SERIAL NO.</b> 10/046,601	
<b>APPLICANT</b> Edward L. Helper							
<b>FILING DATE</b> October 23, 2001				<b>GROUP</b> 2131			

  

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
KA		5,311,176	05/1994	Gurney			
<div style="display: flex; align-items: center; justify-content: center;"> <div style="width: 10px; height: 100px; border-left: 1px solid black; margin-right: 5px;"></div> <div style="font-size: 2em;">↓</div> </div>		5,602,833	02/1997	Zehavi			
		5,751,761	05/1998	Gilhousen			
		6,014,408	01/2000	Naruse et al.			
		6,091,757	06/2000	Cudak et al.			
		6,115,410	09/2000	Naruse			
		6,262,751	07/2001	Chan			

  

FOREIGN PATENT DOCUMENTS								
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
KA		01/58070	08/2001	WO				

  

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		
KA		Kung, "VLSI Array Processors", 1998, pp. 436-438.

<b>EXAMINER</b> /Kaveh Abrishamkar/	<b>DATE CONSIDERED</b> 08/24/2006
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.